



## DIGITAL COMPUTER ORGANIZATION

### SWAYAM Prabha Course Code - E23

<b>PROFESSOR'S NAME</b>	Prof. P.K. Biswas
<b>DEPARTMENT</b>	Electrical Engineering
<b>INSTITUTE</b>	Indian Institute of Technology, Kharagpur
<b>COURSE OUTLINE</b>	Introduction to Digital Computer Organization, CPU Design, CPU Design Timing and Control, Micro programmed Control, Pipeline Concept, Pipeline CPU, Memory Organization, Cache Memory Architecture, RAM Architecture, DAM Architecture, DAM Architecture Buffer Cache, Buffer Cache, Secondary Storage Organization, I/O Subsystem Organization, Error Detection and Correction

#### COURSE DETAILS

S. No	Module ID/ Lecture ID	Lecture Title/Topic
1	117105078Lec01	Introduction to Digital Computer Organization
2	117105078Lec02	CPU Design-I
3	117105078Lec03	CPU Design-II
4	117105078Lec04	CPU Design Timing and Control
5	117105078Lec05	Micro programmed Control-I
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**References if Any:**